

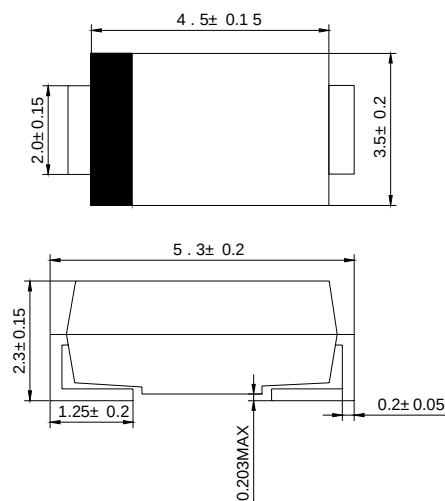
FEATURES

- ◇ Plastic package has underwriters laborator flammability classification 94V-0
- ◇ For surface mounted applications
- ◇ Low profile package
- ◇ Built-in strain relief, ideal for automated placement
- ◇ High temperature soldering:
250°C/10 seconds at terminals

MECHANICAL DATA

- ◇ Case: JEDEC DO-214AA, molded plastic over passivated chip
- ◇ Terminals: Solder plated, solderable per MIL-STD-750, Method 2026
- ◇ Polarity: color band denotes cathode end
- ◇ Weight: 0.003 ounces, 0.093 gram

DO - 214AA(SMB)



Dimensions in millimeters

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified

		RS2A	RS2B	RS2D	RS2G	RS2J	RS2K	RS2M	UNITS
Maximum recurrent peak reverse voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V _{RWS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current @ T _L =90°C	I _{F(AV)}	2.0							A
Peak forward surge current 8.3ms single half-sine-wave superimposed on rated load	I _{FSM}	50							A
Maximum instantaneous forward voltage at 1.5A	V _F	1.30							V
Maximum DC reverse current @T _A =25°C at rated DC blocking voltage @T _A =125°C	I _R	5.0 200							μ A
Maximum reverse recovery time (NOTE 1)	t _{rr}	150				250	500		ns
Typical junction capacitance (NOTE 2)	C _J	18							pF
Typical thermal resistance (NOTE 3)	R _{θJA}	40							°C/W
	R _{θJL}	15							
Operating junction and storage temperature range	T _J T _{STG}	-55-----+150							°C

NOTE: 1. Reverse recovery time test conditions: $I_F = 0.5\text{A}$, $I_R = 1.0\text{A}$, $I_{rr} = 0.25\text{A}$

2. Measured at 1.0MHz and applied reverse voltage of 4.0 Volts

3. Thermal resistance from junction to ambient and junction to lead P.C.B. mounted on 0.2"X0.2" (5.0X5.0mm²) copper pad areas

FIG.1 – FORWARD DERATING CURVE

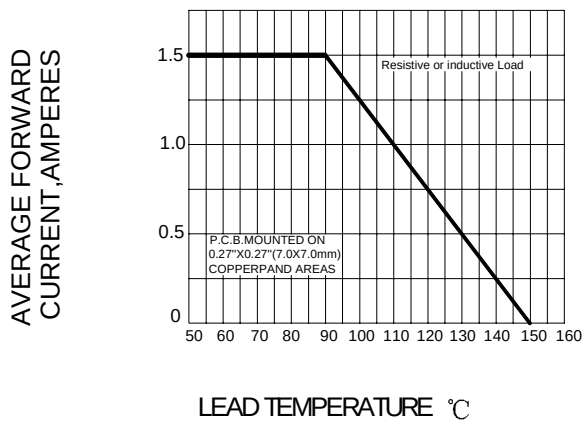


FIG.2 PEAK FORWARD SURGE CURRENT

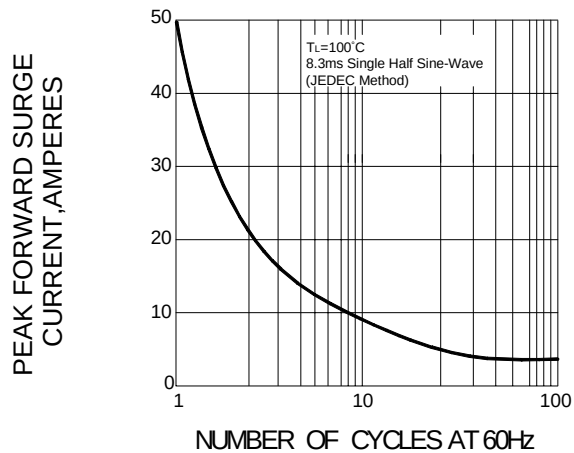


FIG.3 – TYPICAL FORWARD CHARACTERISTICS

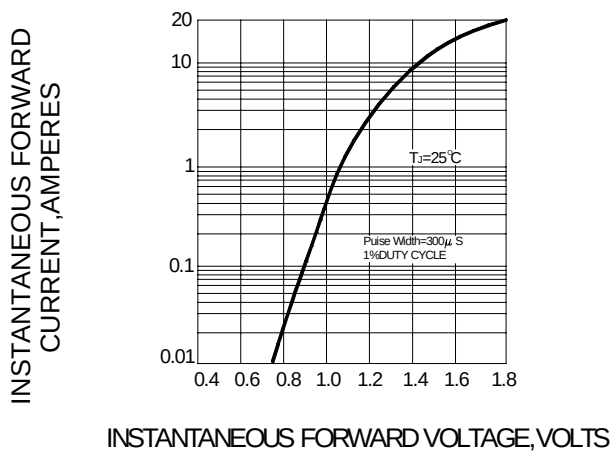


FIG.4 – TYPICAL REVERSE CHARACTERISTICS

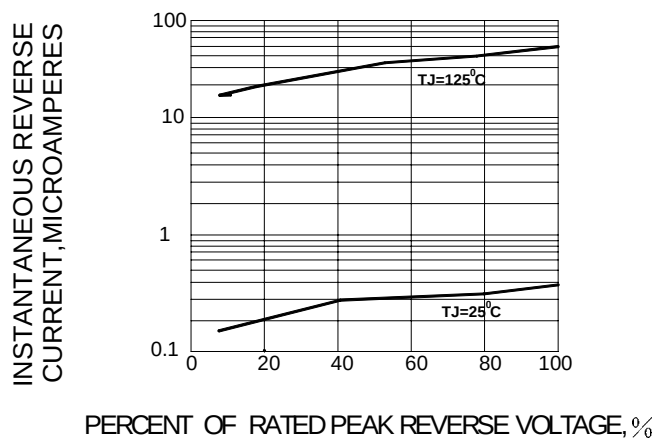


FIG.5-TYPICAL JUNCTION CAPACITANCE

